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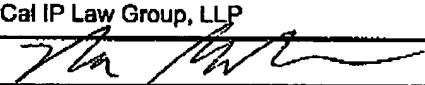
JUN 28 2005

PTO/SB/21 (09-04)

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/044,244	
	Filing Date	01-11-2002	
	First Named Inventor	Alfano	
	Art Unit	2661	
	Examiner Name	McCray	
Total Number of Pages in This Submission	125	Attorney Docket Number	1000-P02161US

ENCLOSURES (Check all that apply)		
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Firm Name	SoCal IP Law Group, LLP	
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Printed name	Mark Andrew Goldstein	
Date	06/28/2005	Reg. No. 50,759

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/044,244 Confirmation No. 9409
Applicant : Alfano
Filed : 01-11-2002
TC/A.U. : 2661
Examiner : McCray
Docket No. : I000-P02161US
Customer No. : 33356

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Dear Sir:

General Matters

<input checked="" type="checkbox"/>	Copies of the references listed on the attached form PTO/SB/08A are enclosed. Copies of US patents and patent applications are not enclosed.
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This Information Disclosure Statement is being filed under:

<input checked="" type="checkbox"/> 37 CFR 1.97(b)(3)	Before the mailing of a first Office action on the merits.
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Respectfully submitted,

Date: June 28, 2005


Mark A. Goldstein, Reg. No. 50,759

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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/044,244
		Filing Date	01-11-2002
		First Named Inventor	Alfano
		Art Unit	2661
		Examiner Name	McCray
Sheet 1 of 3	Attorney Docket Number	I000-P02161US	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)			
		US- 8,026,092	02-15-2000	Abu-Amara	
		US- 6,570,876-B1	05-27-2003	Aimoto	
		US- 2002/0165947-A1	11-17-2002	Ackerman	
		US- 2003/0035427-A1	02-03-2005	Alasti	
		US- 2002/0061022-A1	05-23-2002	Allen	
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		US- 5,550,823	08-27-1996	Irie	
		US- 5,845,145	12-01-1998	James	
		US- 5,898,689	04-17-1999	Kumar	

FOREIGN PATENT DOCUMENTS						
Examiner Initials ²	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
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STATEMENT BY APPLICANT**

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Sheet 2

of 3

Complete if Known

Application Number	10/044,244
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First Named Inventor	Alfano
Art Unit	2661
Examiner Name	McCray
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		Number-Kind Code ² (if known)			
		US- 6,442,674-B1	08-27-2002	Lee	
		US- 5,923,893	07-13-1999	Moyer	
		US- 6,067,408	05-23-2000	Runalduie	
		US- 5,805,911	05-01-1999	Shimizu	
		US- 6,493,347-B2	12-10-2002	Sindhu	
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		US- 6,574,194-B1	06-03-2003	Sun	
		US- 2002/0054602-A1	05-09-2002	Takahashi	
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		Filing Date	01-11-2002
		First Named Inventor	Alfano
		Art Unit	2661
		Examiner Name	McCray
Sheet 3 of 3	Attorney Docket Number	I000-P02161US	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		PETASWITCH SOLUTIONS, INC., The Pisces Chipset, Product Brief, 2001, http://www.peta-switch.com/products/product_brief.htm .	
		PETASWITCH SOLUTIONS, INC., PetaSwitch Solutions Announces Raising \$4 Million in First Round Financing, Press Release, 2001, http://peta-switch.com/newsroom/press_releases.htm .	
		PETASWITCH SOLUTIONS, INC., Company Overview, 2001, http://www.peta-switch.com/markets/overview.htm .	
		GUPTA, Scheduling in Input Queued Switches: A Survey, June 1996, Department of Computer Science, Stanford University, California.	
		SCHOENEN, ET AL., Distributed Cell Scheduling Algorithms for Virtual-Output-Queued Switches, December 1999, pp. 1211-1215, Volume 1, GLOBECOM, IEEE Global Telecommunications Conference.	
		FAHMY, A Survey of ATM Switching Techniques, August 14, 2001, Department of Computer and Information Science, The Ohio State University.	
		STILIADIS, ET AL., Rate-Proportional Servers: A Design Methodology for Fair Queueing Algorithms, December 1995, Computer Engineering & Information Sciences, University of California, Santa Cruz.	
		STOICA, ET AL., Earliest Eligible Virtual Deadline First: A Flexible and Accurate Mechanism for Proportional Share Resource Allocation, Department of Computer Science, Old Dominion University, Norfolk, VA.	

Examiner Signature	Date Considered
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